

Figure 1A

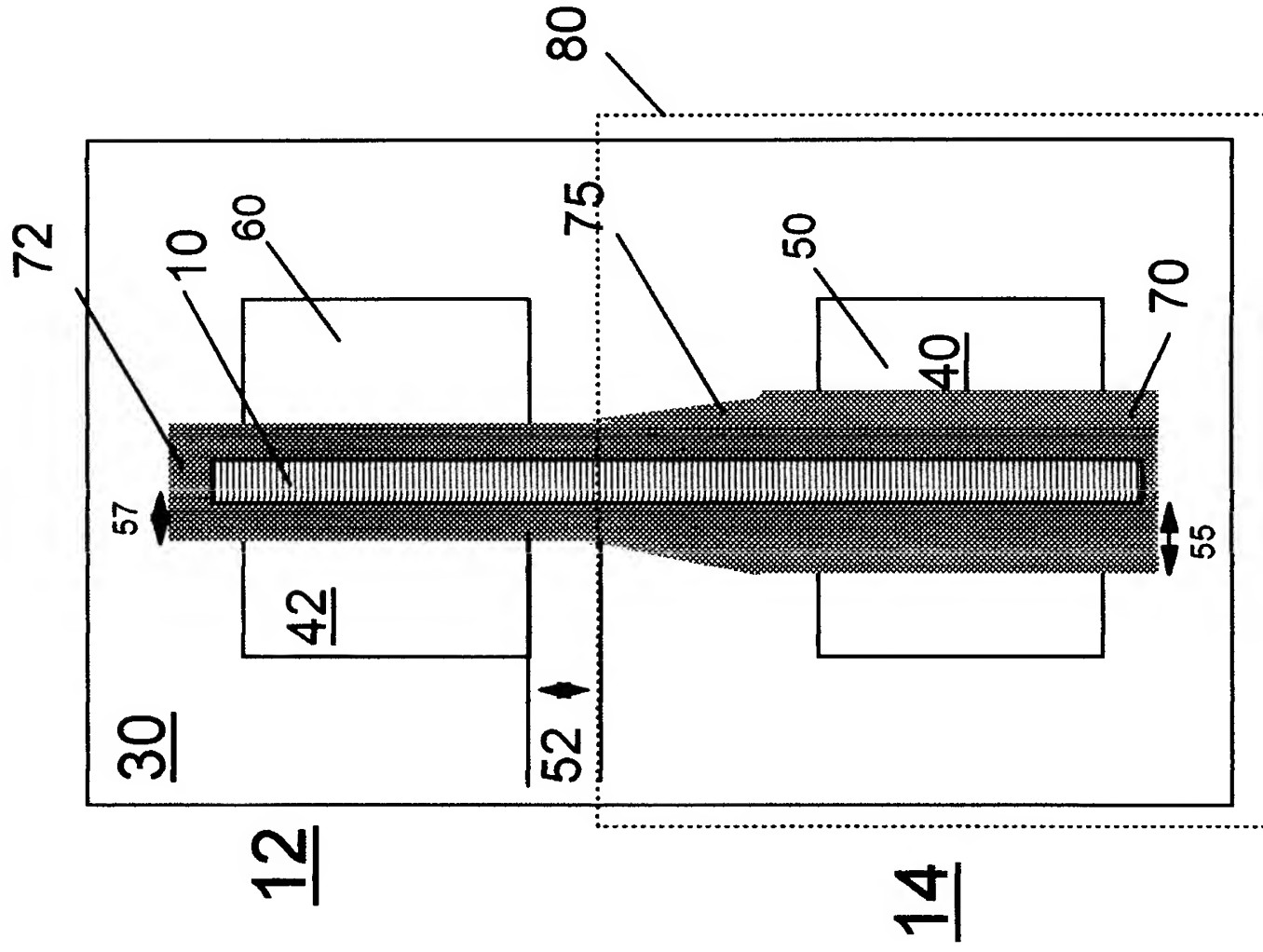


Figure 1B

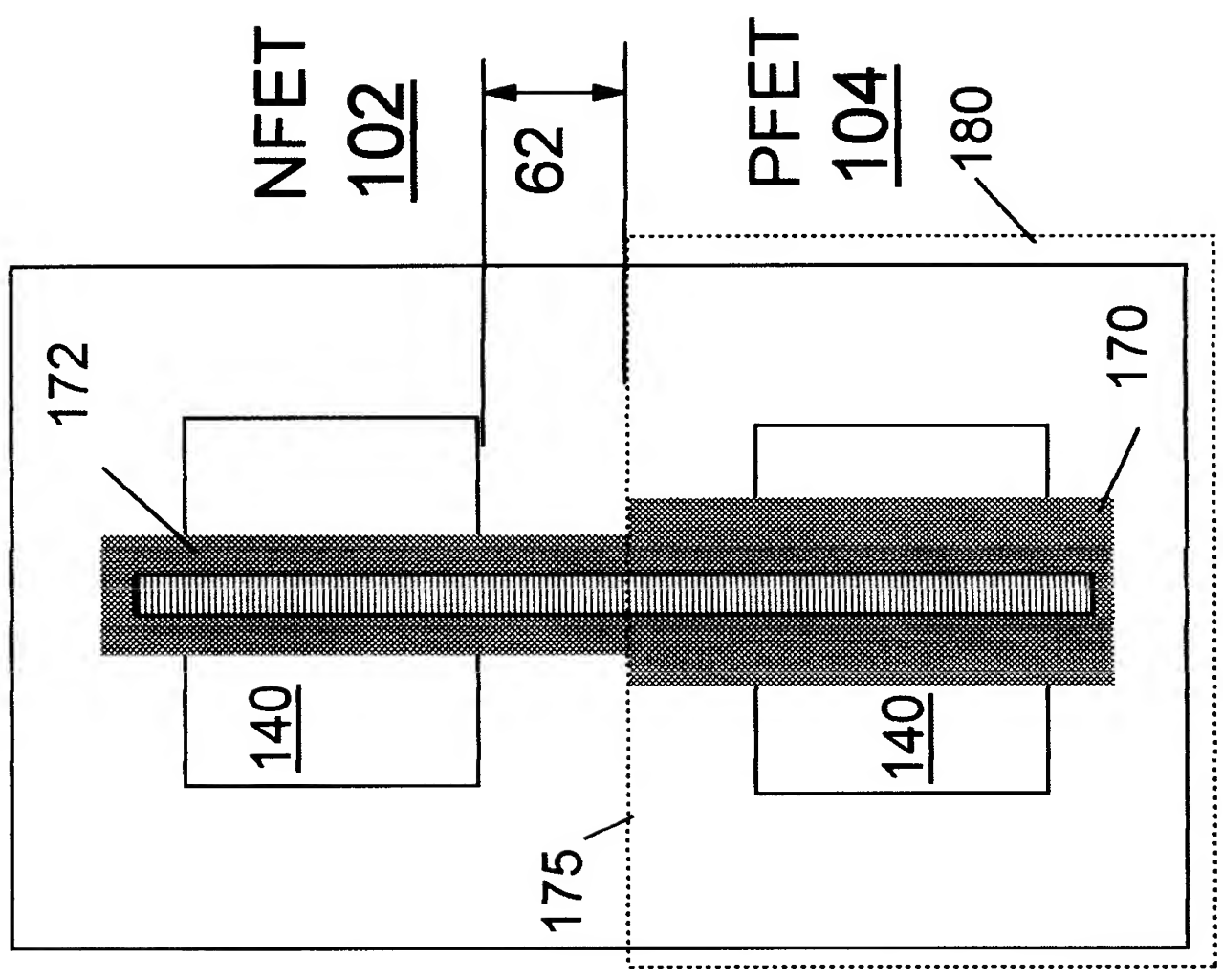


Figure 2

NFET 102

PFET 104

PC Gates

110

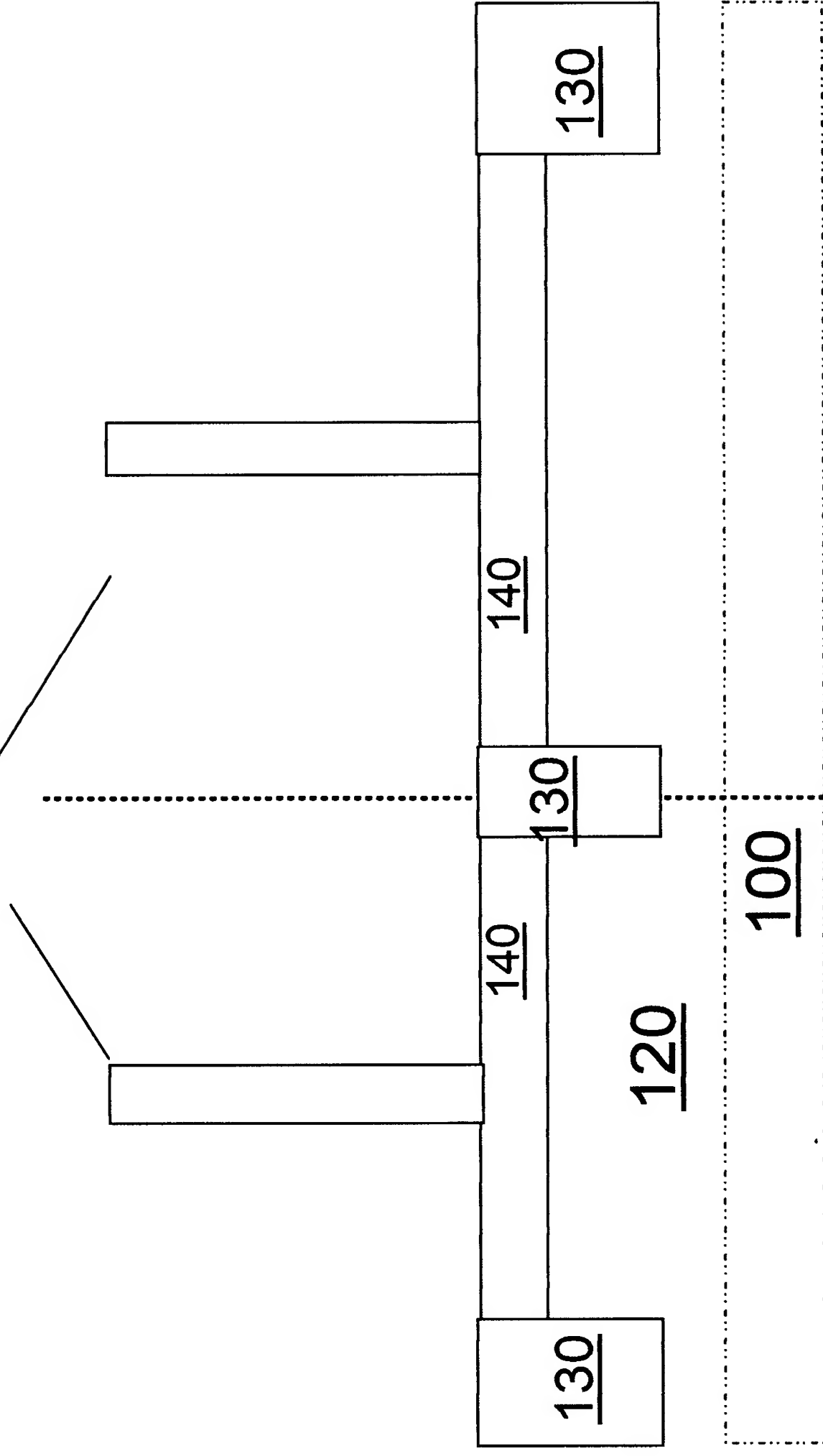


Figure 3

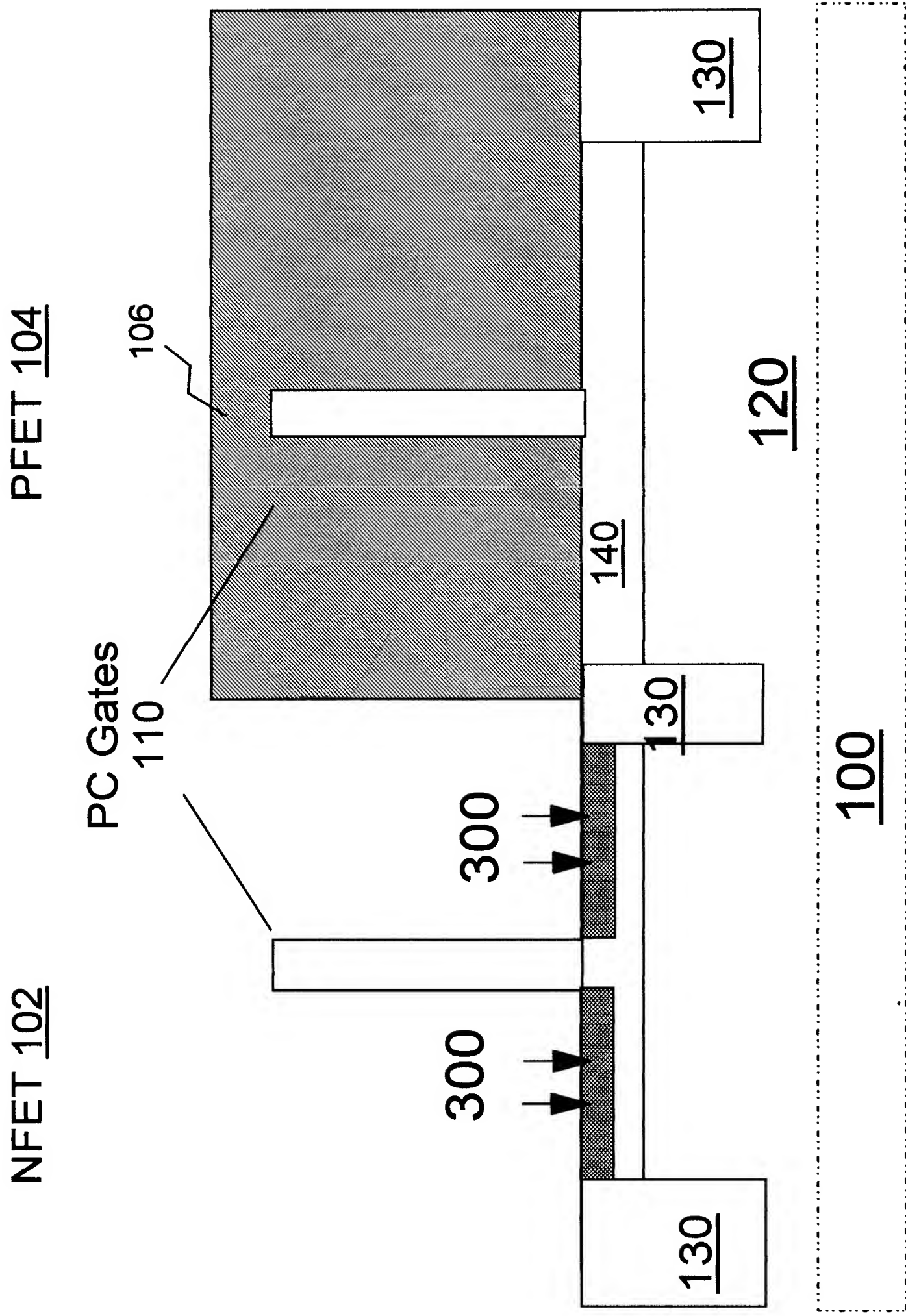


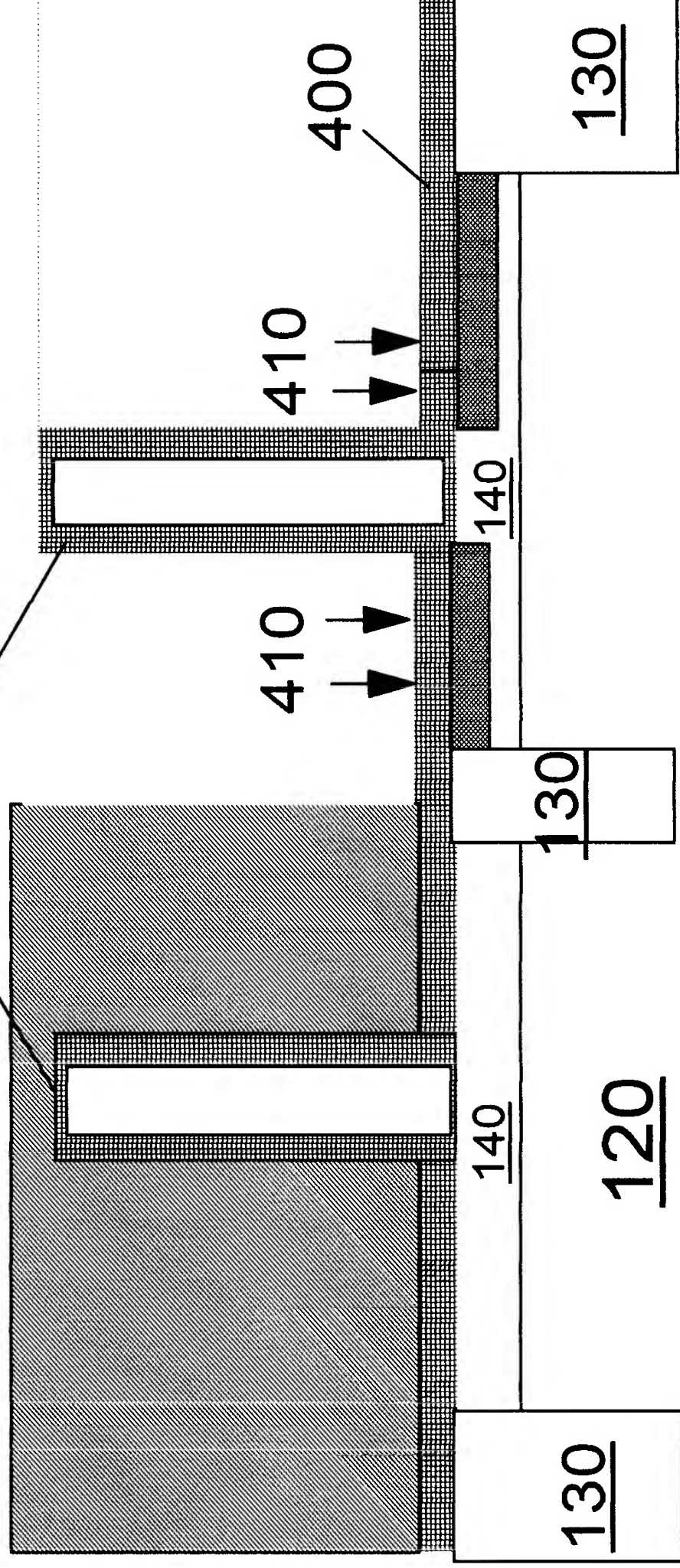
Figure 4

NFET 102

PFET 104

PC Gates

110



100

## Figure 5

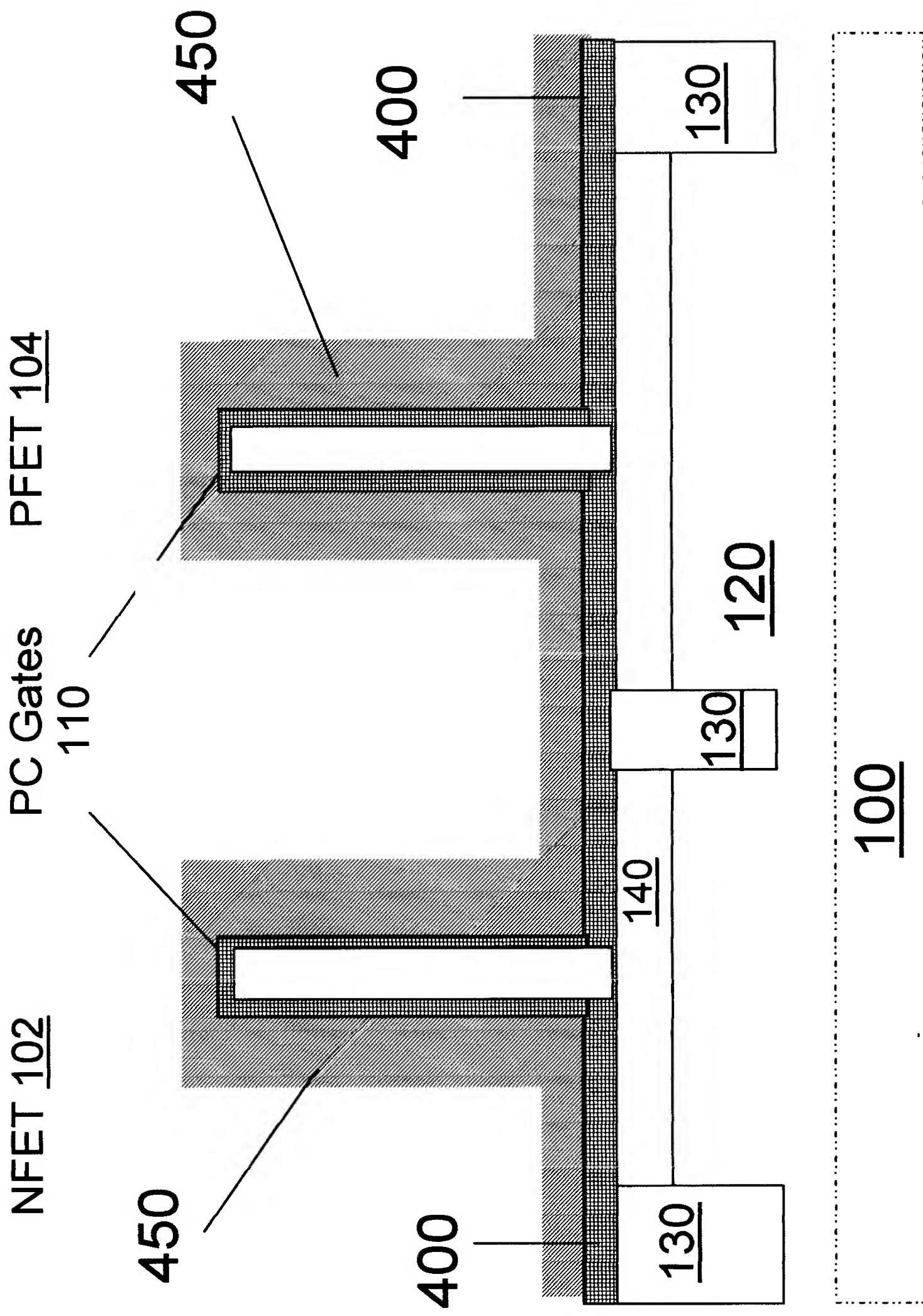


Figure 6

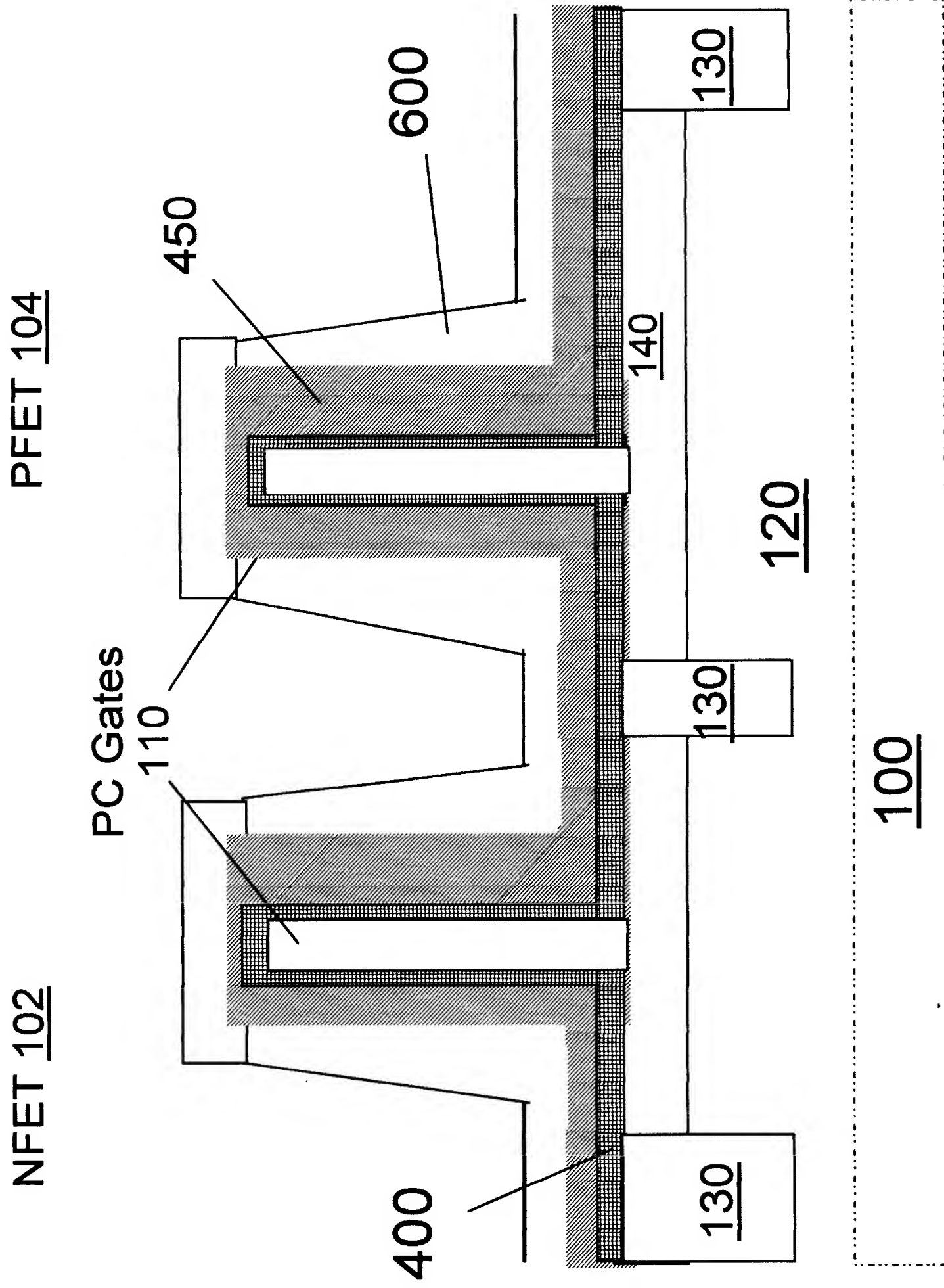


Figure 7

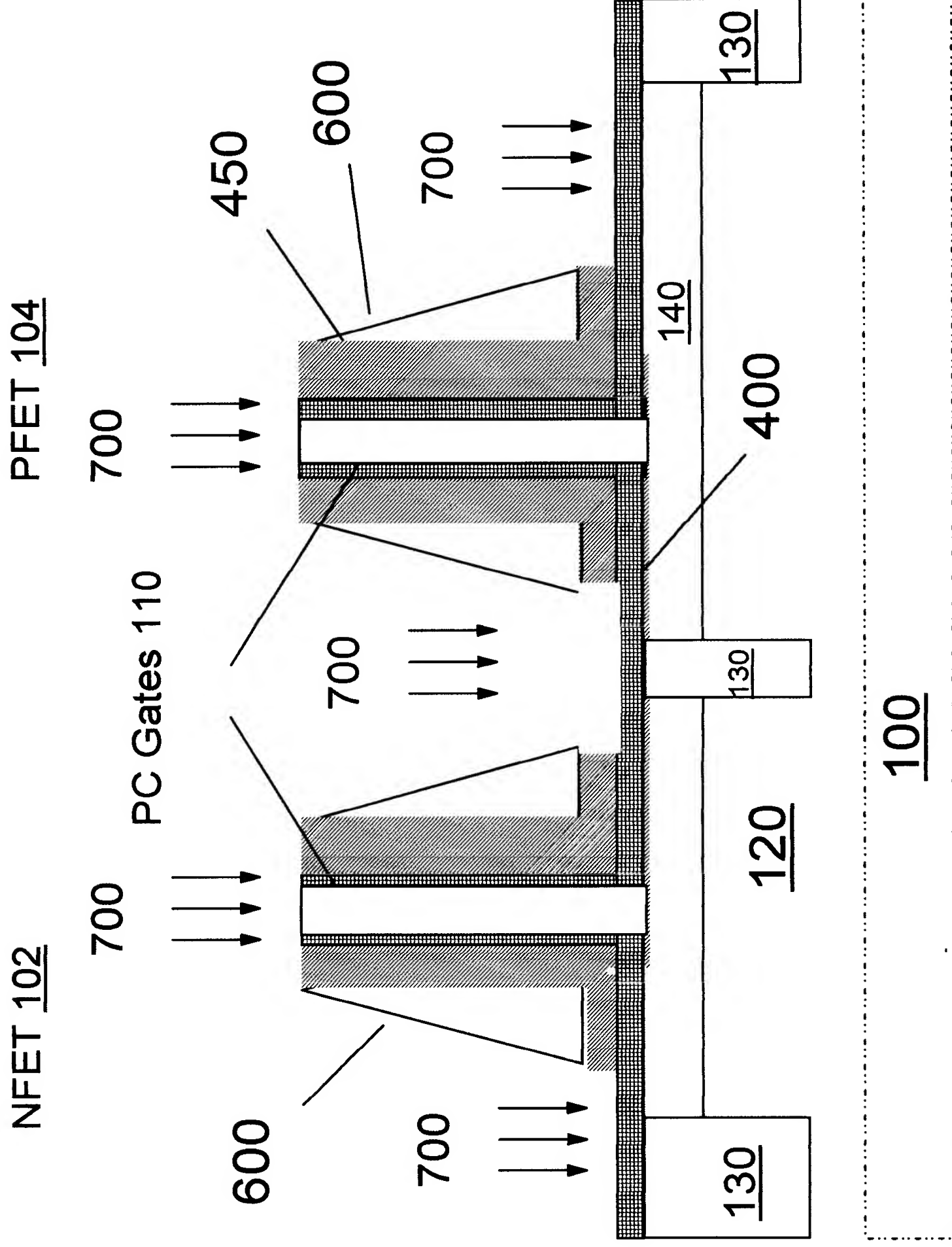
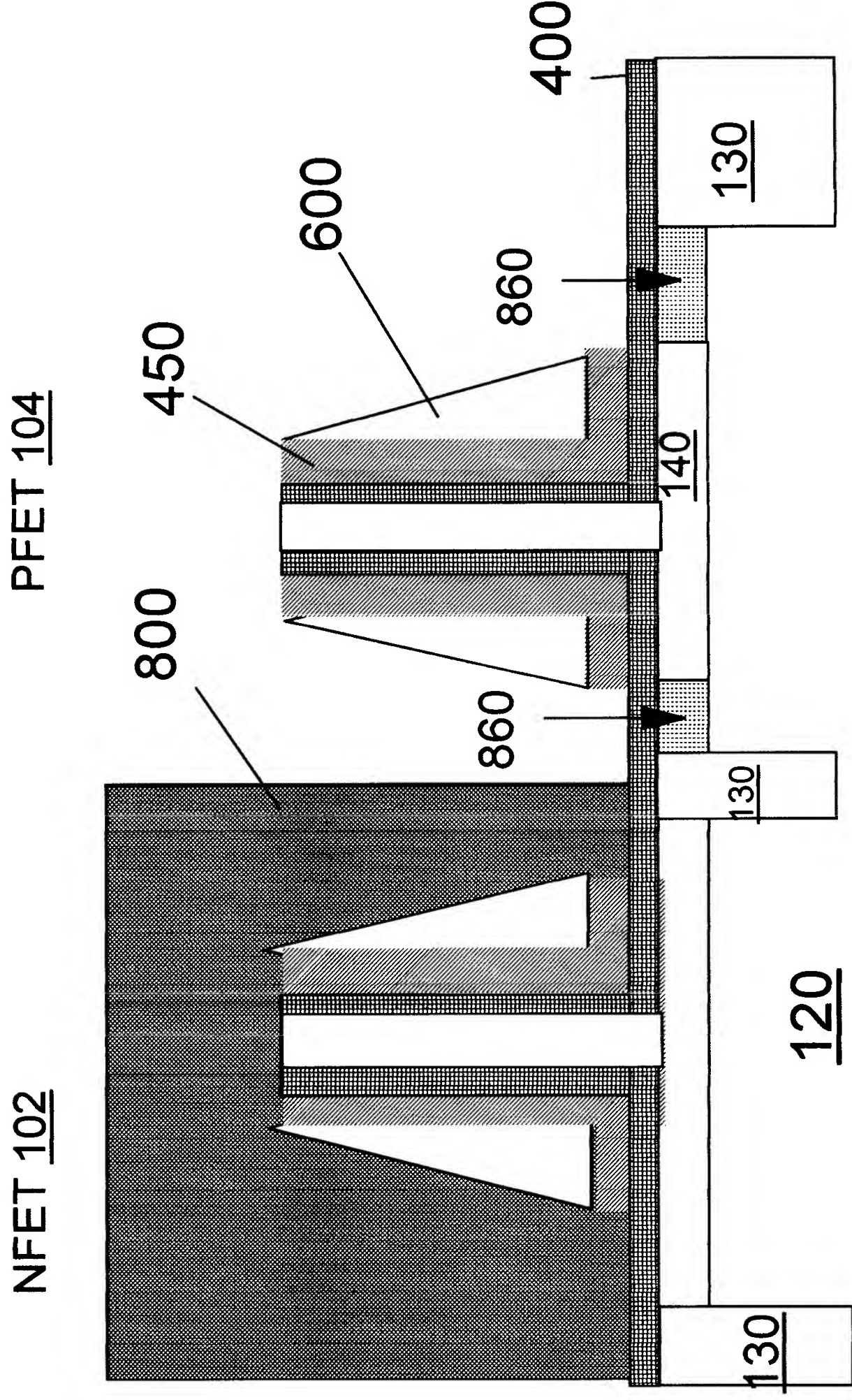




Figure 8



100



Figure 9

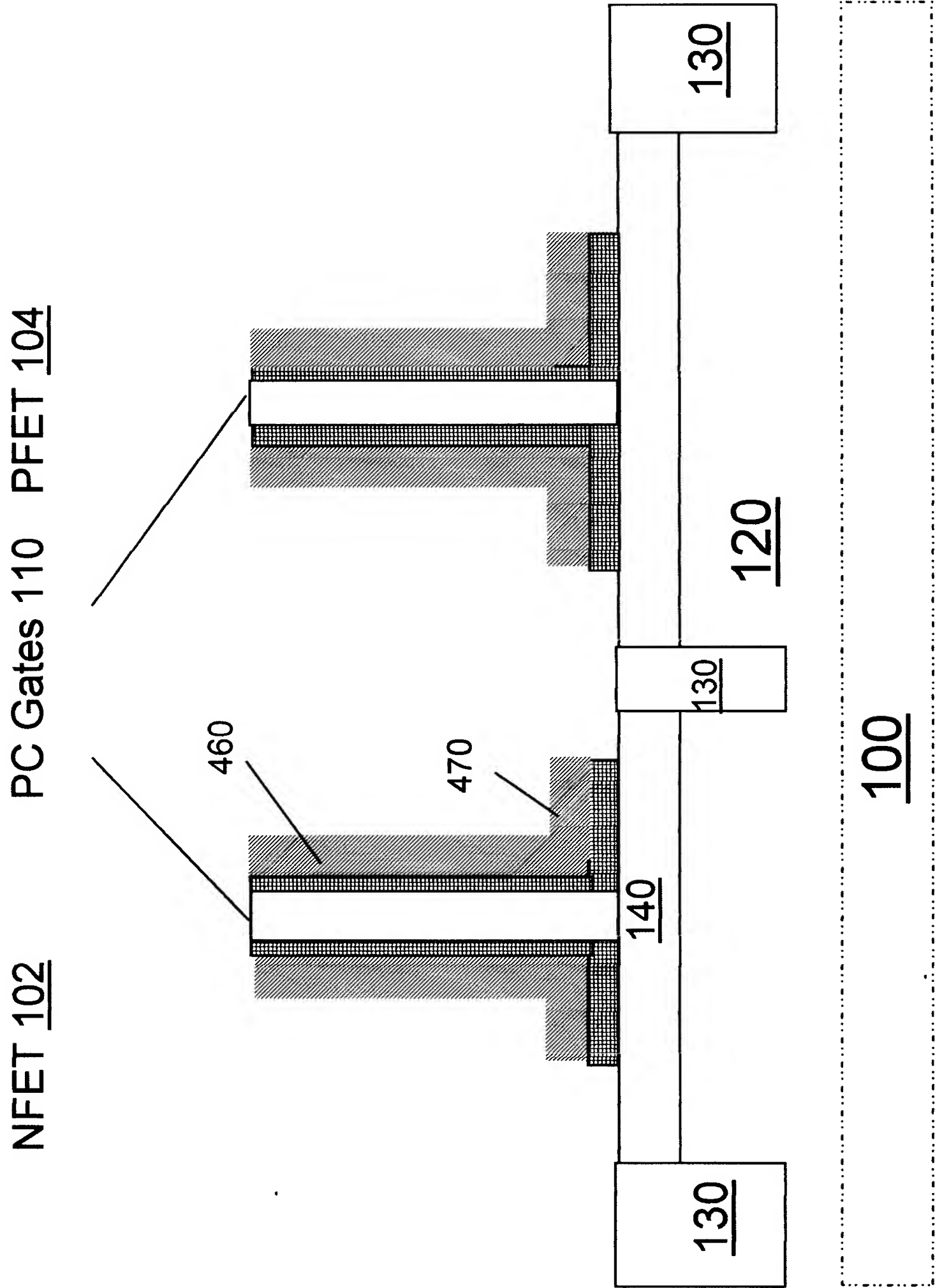
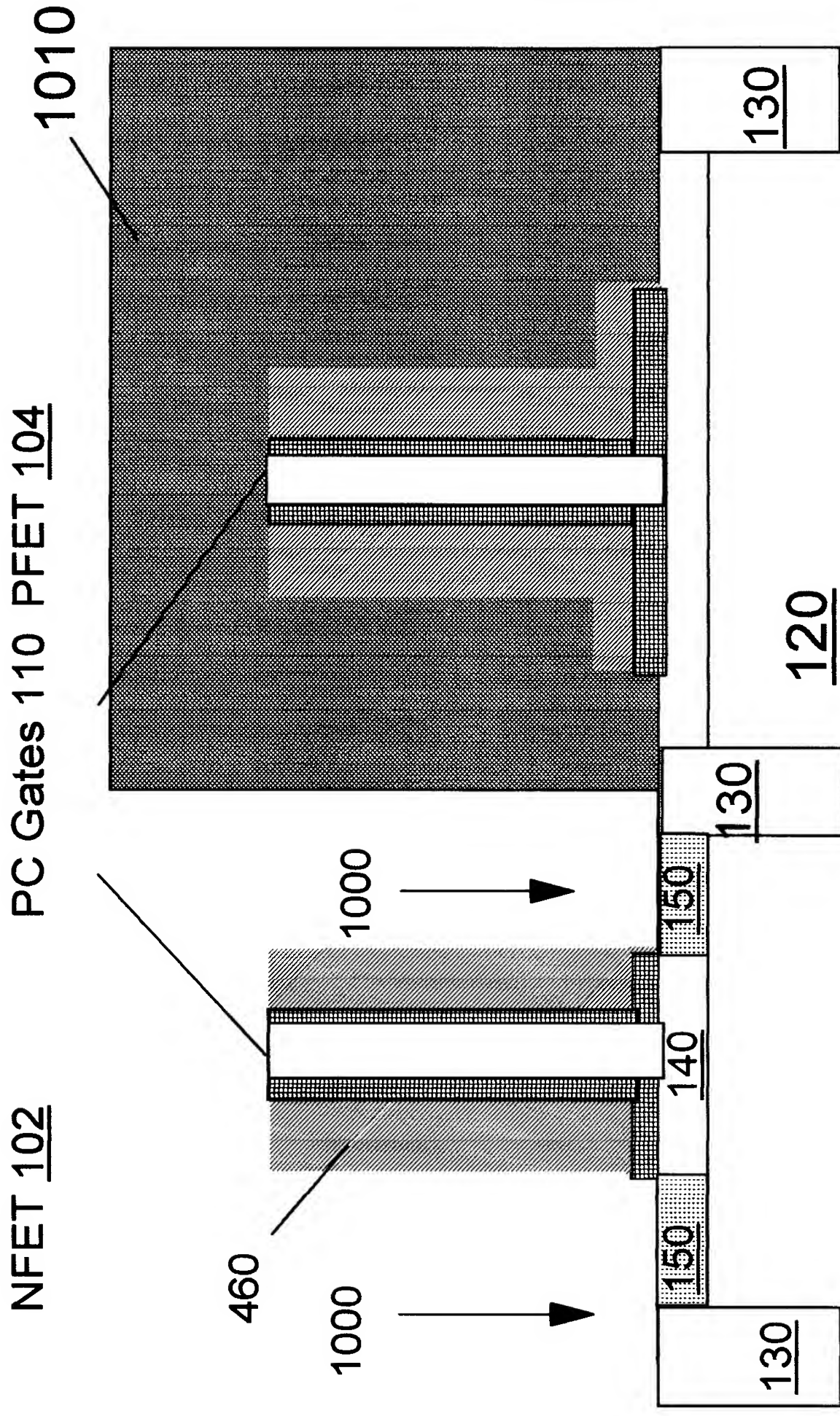


Figure 10



Si Substrate 100

NFET <u>102</u>	PC Gates	PFET <u>104</u>

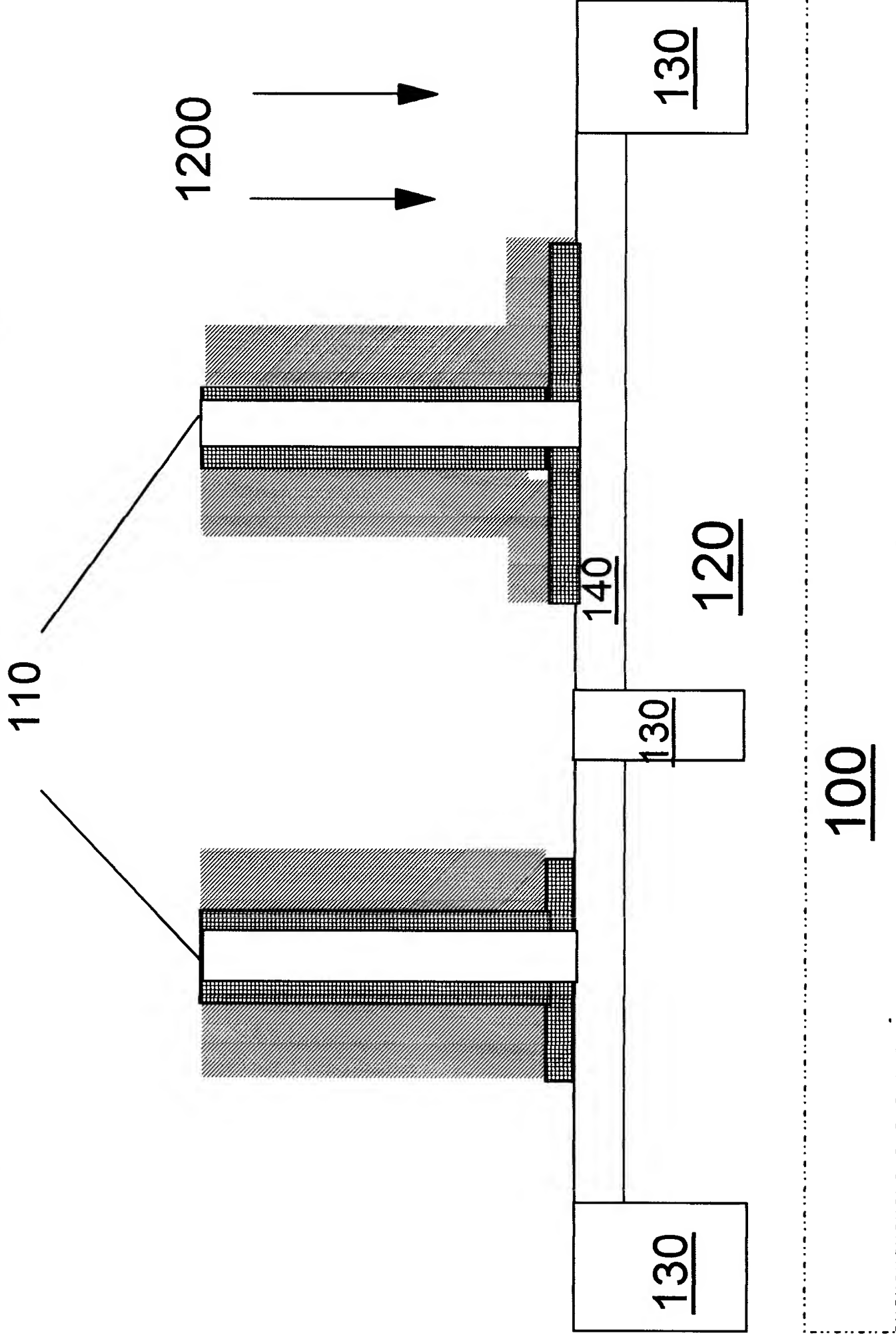


Figure 12

NFET 102

PFET 104

